

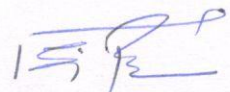


SRI SIDDHARTHA INSTITUTE OF TECHNOLOGY
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 Maralur, Tumkur-572105

Syllabus Prior and Post revision

First year BE

2018 Scheme		2020 Scheme		Remarks
Sub.code	Subject Name	Sub.code	Subject Name	
BS-PH101/201	ENGINEERING PHYSICS	BS-PH104/204	ENGINEERING PHYSICS	20% of the syllabus changed as compared to 2015 scheme 10% of the syllabus changed as compared to 2018 scheme
BS-PH102/202	ENGINEERING PHYSICS LAB	BS-PHL106/206	ENGINEERING PHYSICS LAB	No change
BSCH101/201	ENGINEERING CHEMISTRY	BSCH104/204	ENGINEERING CHEMISTRY	Total hours of the syllabus Changed from 52 to 39 Hours (Credits changed from 4 to 3) According to the credits the 20% of the syllabus is revised by removing Spectroscopy and Periodic Properties and introducing Chapter Smart materials
BSCH102/202	ENGINEERING CHEMISTRY LAB	BSCHL106/206	ENGINEERING CHEMISTRY LAB	Number of Experiments reduced from 14 to 10
18MA101	Calculus and Linear Algebra	BS-MA101	Calculus and Linear Algebra	15% syllabus deviation from 2018 scheme to 2020 scheme We included Engineering Application problems
18MA201	Advanced Calculus and Numerical Methods	BS-MA201	Advanced Calculus and Numerical Methods	5% syllabus deviation from 2018 scheme to 2020 scheme
		ES-CE103/203	Elements of civil engineering	Subject was reintroduced in 2020 scheme


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Second year BE

2018 Scheme		2020 Scheme		Remarks
Sub.code	Subject Name	Sub.code	Subject Name	
18MA301	Integral Transforms and Numerical Techniques	MA3CS1/M A3IS1	Statistics and Probability	100% syllabus deviation from 2018 scheme to 2020 scheme. Revised the syllabus branch-wise as per the request of concerned departments.
18MA301	Integral Transforms and Numerical Techniques	MA3TH1	Integral Transforms and Numerical Techniques	10% syllabus deviation from 2018 scheme to 2020 scheme. Revised the syllabus branch-wise as per the request of concerned departments.
18CS401	Probability and Queuing Theory	MA4CS1/M A4IS1	Combinatorics and Linear Algebra	100% syllabus deviation from 2018 scheme to 2020 scheme. Revised the syllabus branch-wise as per the request of concerned departments.
18MA401	Probability and Calculus of variation	MA4TH1	Probability and Calculus of variation	30% syllabus deviation from 2018 scheme to 2020 scheme. Revised the syllabus branch-wise as per the request of concerned departments.
18ES401	Probability and Complex Analysis	MA4ES1	Probability and Complex Analysis	15% syllabus deviation from 2018 scheme to 2020 scheme. Revised the syllabus branch-wise as per the request of concerned departments.
18CSI302	Programming with Python	CS3TH2	Object Oriented Programming	Integrated lab has been removed and a separate lab course is introduced to give more waightage. 5% of syllabus has been modified or deleted as we kept the prior syllabus for 3 sem Data structures as a prerequisite to algorithms
18CS303	Data Structures	CS3TH3	Data Structures	10% of the syllabus has been changed and added prerequisite topics for Algorithms.
18CS304	Analog and Digital Electronic Circuits	CS3TH4	Discrete Mathematical Structures	Since the elements of FSM are described in set notations FSM is added to DMS to illustrate that the concepts of DMS can be applied to describe FSM. Topics such as groups and rings, Recurrence relations, Generating functions have been

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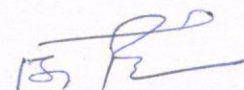


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				removed and introduced concept of Finite Automata. Around 20 percent of syllabus has been changed.
18CS305	Discrete Mathematical Structures	CS3TH5	Digital Circuit Design and its Applications	40% of the syllabus has been modified. Changed from ADEC to DCDA (2018) Analog Electronics was removed as it is included in First year basic Electronics of 2020 scheme
18CS306/	Computer Organization / Fundamental Mathematics	CS3TH6/ MA3DP6	Computer Organization / DIP Mathematics	Two topics have been removed less than 5%, those topics are going to be covered in detail in the higher semesters
18DIP306	(Mathematics for Diploma students)	CS3LB1	Object Oriented Programming Lab	In 2018 scheme it was an integrated lab. In order give more weightage the lab course is seperated.
18CS307	Data Structures Lab	CS3LB2	Data Structures Lab	No changes
18CS308	Analog and Digital Electronic Circuits Lab	CS3LB3	Digital Circuit Design Lab	80% of the Hardware based lab experiments were removed and added more software based lab experiments. Overall 50% of the syllabus changed
18CS401	Probability and Queuing Theory	MA4CS1/M A4IS1	Combinatorics and Linear Algebra	Revised the syllabus as per the computer science dept requirement (100%)
18CSI402	Object Oriented Programming	CS4TH2	Design and Analysis of Algorithms	5% of syllabus has been modified or deleted as we kept the prior syllabus for 3 sem Data structures as a prerequisite to algorithms
18CS403	Design and Analysis of Algorithms	CS4TH3	Operating Systems	


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18CS404	Operating Systems	CS4TH4	Programming with Python	Removed integrated subject and created as new theory component. Syllabus is totally revised compared to Programming with Python CSI302. Python packages - NumPy, Pandas and Matplotlib are added. 40 % of the syllabus is revised
18CS405	Microprocessor and Embedded System	CS4TH5	Automata Theory and Computation	To give more importance to the concept of Theory of computations some advance topics have been added. 20 percent of the syllabus has been modified
18CS406	Data Communication	CS4TH6	Software Engineering	
18CS408	Microprocessor and Microcontroller Lab	CS4LB2	Operating Systems Lab	Introduced new cycles of experiments.
18SK401	Skill Development - II	CS4LB3	Python Programming Lab	Introduced separate lab with new cycles of experiments.
		SK4DP2	Skill Development-II	No changes
18EEI302	Analog Electronic Circuits	EE3TH2	Analog and Digital Electronics	Subjects merged to Accommodating new courses
18EE307	Circuit Simulation and Measurements Lab	EE3LB1	Analog and Digital Electronics Lab	Subjects merged to Accommodating new courses
18EEI402	Digital Electronic Circuits	EE4TH2	Power Electronics	Subject shifted from 5 th sem to 4 th sem
18CEI302	Applied Engineering Geology	CE3TH2	Applied Mechanics	
18CE303	Applied Mechanics	CE3TH3	Engineering Geology	
18CE305	Survey Theory	CE3TH5	Building Planning and Drawing	In the previous scheme the subject was in 4 th sem

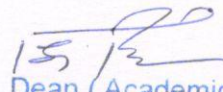
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18CE306/ 18DIP300	Construction Technology and Building Materials/ Fundamental Mathematics	CE3TH6/M A3DP6	Surveying / DIP Mathematics	Other than lateral entry students / for lateral entry students
18CE307	Survey Practice - I	CE3LB1	Hydraulics and Hydraulic Machines Lab	
18CE308	Hydraulics & Hydraulic Machinery Laboratory	CE3LB2	Surveying Practice - I	
		CE3LB3	Auto CAD Lab	An additional lab was introduced
18CE402	Building Planning and Drawing	CE4TH2	Strength of Materials	
18CE403	Strength of Materials	CE4TH3	Concrete Technology	
18CE405	Concrete Technology	CE4TH5	Building materials and construction management	A new subject was introduced in 4 th sem
18EC406	Basic Electronics Engineering	CE4TH6	Basic Geotechnical Engineering	Basic Electronics was introduced in first year, so core subject was shifted from 5 th to 4 th sem
18CE4L01	Surveying Practice - II	CE4LB1	Basic Material Testing Lab	
18CE4L02	Basic Materials Testing Laboratory	CE4LB2	Surveying practice - II	
		CE4LB3	Engineering Geology Lab	An exclusive lab was reintroduced which was earlier a integrated.
18ECI302	Signals and Systems	ET3TH2	Signals and systems	20 % Deviation from 2018 to 2020 Scheme
18EC303	Network Theory	ET3TH3	Network analysis	10 % Deviation from 2018 to 2020 Scheme
18EC304	Analog Electronic Circuits	ET3TH4	Analog Electronic circuits	5 % Deviation from 2018 to 2020 Scheme


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18EC305	Digital Electronic Circuits	ET3TH5	Digital Electronic Circuit Design using Verilog	30 % Deviation from 2018 to 2020 Scheme
18EC306	Electronic Instrumentation	ET3TH6	Electronic Instrumentation	No change in syllabus.
		ET 3LB1	Signals and Systems Lab	New Lab course-Earlier it was an integrated lab with 18ECI302
18EC307	Analog Electronic Circuits Lab	ET 3LB2	Analog Electronic circuits Lab	30 % Deviation from 2018 to 2020 Scheme
18EC308	Digital Electronics Circuits Lab	ET 3LB3	Digital Electronics Circuits Lab	20 % Deviation from 2018 to 2020 Scheme. Verilog programming is added.
18ECI402	Digital System Design using Verilog			Merged with Digital Electronics Circuits, ET3TH5
		ET4TH2	Sensors and Applications	New course
18EC403	Fields and Waves	ET4TH3	Fields and Waves	10 % Deviation from 2018 to 2020 Scheme
18TC404	Analog and Digital Communication	ET4TH4	Analog and Digital Communication	20 % Deviation from 2018 to 2020 Scheme
		ET4LB1	Sensors and Applications Lab	New Lab course
18CSI302	Programming with Python	IS3TH2	Discrete Mathematical Structures	20% modification
18CS303	Data Structures	IS3TH3	UNIX and SHELL Programming	Newly introduced according to industry need
18CS304	Analog and Digital Electronic Circuits	IS3TH4	Data Structures and its Applications	10% changes
18CS305	Discrete Mathematical Structures	IS3TH5	Object Oriented Programming	10% changes

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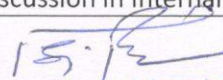


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18CS306/ 18DIP306	Computer Organization / Fundamental Mathematics (Mathematics for Diploma students)	IS3TH6/MA 3DP6	Digital Design and Computer Organization/Fundamental Mathematics	Digital Electronics and Computer Organization subjects merged
18CSL307	Data Structures Lab	IS3LB1	UNIX and SHELL Programming Laboratory	Newly introduced
18CSL308	Analog and Digital Electronics Circuits Lab	IS3LB2	Data Structures Laboratory	10-15% changes
18SK301	Skill Development – I	IS3LB3	Object Oriented Programming Laboratory	10-15% changes
18CS404	Operating Systems	IS4TH4	Computer Networks-1	In 2016 scheme, this subject was named as Data Communication in 5th sem. Moved to 4th sem with new name Computer Networks-I
18CS405	Microprocessor and Embedded System	IS4TH5	Python for Data Processing	In 2016 scheme, this subject was in 3rd sem, now moved to 4th sem with 20% changes as a prerequisite for Machine Learning
18CSL408	Microprocessor and Microcontroller Lab	IS4LB2	Data Processing Laboratory	Newly introduced as a prerequisite for Machine Learning Lab
18ECI302	Signals and Systems	EC3TH2	Signals and Systems	Integrated Course separated into Theory Course and Lab Course. As per CDC Guidelines of SSIT-Tumkur, introduction of 3 Lab Courses for 2nd Year BE. Course (Theory) Contents reduced from 52 Hours (4 Credits) of Teaching to 39 Hours (3 Credits) of Teaching. 10 Experiments are introduced for the Lab Course from the Theory Contents.
18EC303	Network Theory	EC3TH3	Network Theory	Course Contents reduced from 52 Hours (4 credits) of Teaching to 39 Hours (3credits) of Teaching as per discussion in Internal BOS meeting.
18EC304	Analog Electronic Circuits	EC3TH4	Analog Electronic Circuits	Course Contents reduced from 52 Hours (4 credits) of Teaching to 39 Hours (3credits) of Teaching as per discussion in Internal BOS meeting.


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		EC3LB1	Signals and Systems Lab	New Lab Course introduced (separated from Integrated Course). Total 10 Experiments in Lab Course, extracted from Theory Course
18EC307	Analog Electronics Circuits Lab	EC3LB2	Analog Electronics Circuits Lab	Lab Course Contents changed as per Credits changed from 1 to 1.5. Introduce design aspects in lab course.
18EC308	Digital Electronics Circuits Lab	EC3LB3	Digital Electronics Circuits Lab	Lab Course Contents changed as per Credits changed from 1 to 1.5. Introduce design aspects in lab course.
18ECI402	Digital System Design using Verilog	EC4TH2	Digital System Design using Verilog	Integrated Course separated into Theory Course and Lab Course. As per CDC Guidelines of SSIT-Tumkur, introduction of 3 Lab Courses for 2nd Year BE. Course (Theory) Contents reduced from 52 Hours (4 Credits) of Teaching to 39 Hours (3 Credits) of Teaching. 10 Experiments are introduced for the Lab Course from the Theory Contents.
18EC403	Fields and Waves	EC4TH3	Fields and Waves	Revised the syllabus as per the credit changed from 4 Credits (52 Hours) to 3 Credits (39 Hours), around 20% syllabus reduced.
18EC404	Analog Communication	EC4TH4	Analog Communication	Revised the syllabus as per the credit changed from 4 Credits (52 Hours) to 3 Credits (39 Hours), around 20% syllabus reduced.
18EC405	Microcontroller and Its Applications	EC4TH5	Microcontroller and Its Applications	Review the syllabus as per the discussion at Internal BOS meeting, around 5% syllabus changed.
18EC406	Control Systems	EC4TH6	Control Systems	Review the syllabus as per the discussion at Internal BOS meeting, around 5% syllabus changed.
		EC4LB1	Verilog Lab	New Lab Course introduced (separated from Integrated Course). Total 10 Experiments in Lab Courses, extracted from Theory Course


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Third year BE

2018 Scheme		2020 Scheme		Remarks
Sub.code	Subject Name	Sub.code	Subject Name	
18EE501	Power Electronics	EE5TH1	PLC and SCADA	New subject added
18CS501	Computer Networks	CS5TH1	Web Technologies	To introduce the new concepts(20%)
18CSI502	Software Engineering	CS5TH2	Object Oriented Modelling and Design	Previously it was elective, now it is core subject since it is important computer science subject
18CS503	Introduction to Automata Theory and Computation	CS5TH3	Data Communication	Core subject CSE STUDENTS NEED TO STUDY
18CS504	Database Management System	CS5TH4	Database Management System	30% of Syllabus has been modified. Relational algebra part has been removed and given more importance to SQL as well as new trend topic i.e NOSQL has been added.
18CS5PE5x	Professional Elective-I	CS5PE5x	Professional Elective-I	provides more detailed explanations and comprehensive examples on each topic to design advanced system calls and programming skills.
18CS5OE6x	Open Elective-I	CS5OE6x	Open Elective-I	With syllabus content help the student for industry standar
18CS507	Computer Networks Lab	CS5MP1	Mini Project-I	Since mini project is introduced as a separate component , mini Project has been deleted from database lab and 2 more programs is added.
18CS508	Database Lab with Mini Project	CS5LB1	Database Lab	Since mini project is introduced as a separate component , mini Project has been deleted from database lab and 2 more programs is added.
18CS601	Compiler Design	CS6TH1	Artificial Intelligence and Machine Learning	All of the relevant topics are covered.
18CSI602	Web Technologies	CS6TH2	Compiler Design	Credits reduced from 4 to 3. So two of the extra parsing algorithms are removed. 10% reduced. The Topics CLR and LALR parsing algorithms are removed.
18CS603	Artificial Intelligence and Machine Learning	CS6TH3	Computer Networks	
18CS6PE4x	Professional Elective-II	CS6PE4x	Professional Elective-II (IOT and Data Science)	Introduced as new subject relavent to industry treands
18CS6MP01	Mini Project	CS6MP1	Mini Project-II	Introduced as new project lab
18CS607	Compiler Design Lab	CS6LB1	Machine Learning Lab	100% syllabus changed. Introduced the implementation of basic ML algorithms using python
18CS608	Machine Learning Lab	CS6LB2	Computer Networks Lab	20% of the syllabus has been changed



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18CEI502	Geotechnical Engineering	CE5TH2	Geotechnical Engineering –II	This subject was reintroduced.
		CE5MP1	Mini Project-I	Extensive survey camp is divided as MP1 & MP2
18CE507	Concrete and Highway Laboratory	CE5LB1	Geotechnical Engineering Lab	An exclusive lab was shifted from 6 th sem to 5 th sem.
18CE508	Software Engineering Lab	CE5LB2	Concrete & Highway Lab	
18CEI602	Environmental Engineering	CE6TH2	Design and Drawing of RC Structure	Hydraulic Structures and Irrigation design was replaced.
18CE603	Hydraulic Structures and Irrigation Design	CE6TH3	Environmental Engineering	
18TC501	Digital Signal Processing and its Applications.	ET5TH1	Information Theory and Error control coding	No change in syllabus. Change of Subject code
18TCI502	Information Theory and Error Control Coding	ET5TH2	Digital Signal Processing	No change in syllabus. Change of Subject code
18TC503	Microwaves and Antennas	ET5TH3	Microwaves and Antennas	No change in syllabus.
18TC504	Management and Entrepreneurship	ET5TH4	Management and Entrepreneurship	5% Deviation from 2018 to 2020 Scheme
18TC5PE51	Digital Switching Systems	ET5PE51	Digital Switching Systems	10 % Deviation from 2018 to 2020 Scheme
18TC601	Wireless and Mobile Communication	ET 6TH1	CMOS VLSI Design	
18TCI602	OOPS with C++	ET 6TH2	Wireless and Mobile Communication	
18TC603	CMOS VLSI Design	ET 6TH3	OOP with C++	
18TC608	Communication Systems Lab	ET 6LB1	Communication Systems Lab	New Lab course

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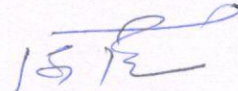
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18ISI502	Advanced Java and J2EE	IS5TH2	Advanced Java and J2EE	10% changes
18CS503	Introduction to Automata Theory and Computation	IS5TH3	Database Management Systems	
18CS504	Database Management Systems	IS5TH4	Introduction to Automata Theory and Computation	
18ISL507	Operating Systems Lab	IS5MP1	Mini Project-I	Introduced Mini project both in 5 th and 6 th sem to give more exposure towards projects
18CSL508	Database Lab with Mini Project	IS5LB1	Computer Networks Laboratory	10% changes
18SK501	Skill Development-III	IS5LB2	Database Laboratory	10% changes
18CSI602	Web Technologies	IS6TH1	Web Technologies	30% change according to industry needs
18CS603	Artificial Intelligence and Machine Learning	IS6TH2	Software Engineering and Software Testing	Software Engineering and Software Testing subjects merged
18ISL607	Computer Networks Lab	IS6MP2	Mini Project-II	Introduced Mini project both in 5 th and 6 th sem to give exposure towards projects
18CSL608	Machine Learning Lab	IS6LB1	Web Technology Laboratory	New programs introduced in a cycle basis as per the theory concepts
18EC501	Digital Signal Processing	EC5TH1	Digital Communication	Revised the syllabus as per the credit changed from 4 Credits (52 Hours) to 3 Credits (39 Hours), around 20% syllabus reduced.
18ECI502	Information Theory and Coding	EC5TH2	Embedded System Design	New Course introduced, instead of Earlier course. Content of the new course for 39 Hours of Teaching (3 Credits)
18EC503	Fundamentals of CMOS VLSI Design	EC5TH3	Fundamentals of CMOS VLSI Design	Contents of the course reduced from 52 Hours (4credits) teaching to 39 hours of Teaching (3 credits)
18EC504	Microwave Communication	EC5TH4	Microwave Theory and Antenna	Contents of the course changed, introduced more antenna concepts (reduced microwave devices principle concepts). Nearly 30 % of the contents changed.
		EC5MP1	Mini Project-1	Mini project-1 introduced
18EC507	Digital Signal Processing Lab	EC5LB1	Digital Signal Processing Lab	5% of the contents enhanced and design concept introduced due to change in credits from 1 to 1.5 credits
18EC508	VLSI Lab	EC5LB2	VLSI Lab	5 % of the contents changed and design concept introduced due to change in credits from 1 to 1.5 credits
18EC601	Digital Communication	EC6TH1	Digital Communication	20% of the syllabus changed and reduced. The credits changed from 4 (52 hours of Teaching) to 3 credits (39 hours of Teaching) and reviewed some of the topics, like error control coding.



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18ECI602	Digital System Design	EC6TH2	Digital Image Processing	New Course introduced as per the discussion in Internal Bos meeting. Digital system design replaced by Digital Image Processing
18EC603	Computer Communication Networks	EC6TH3	Computer Communication Networks	Reviewed the complete syllabus and reduced the syllabus by 20%, credits reduced from 4 to 3 credits. New topics are introduced.
18EC6PE4x	Professional Elective-II	EC6PE4x	Professional Elective-II	3 Elective Courses in the group, Artificial Intelligence (AI) introduced, instead of Speech Processing, one new course introduced
18EC6OE5x	Open Elective-II	EC6OE5x	Open Elective-II	Domain specific elective courses introduced; new open elective courses are introduced: 1. Robotics 2 Pattern Recognition
18EC6MP01	Mini Project	EC6MP2	Mini Project-II	More weightage given to Mini Project-II, includes hardware design aspects and Algorithm development aspects.
18EC608	CCN Lab	EC6LB2	CCN Lab	5% of the content is enhanced and reviewed the experiments title and design aspects are introduced.


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First year M.Tech

2018 Scheme		2020 Scheme		Remarks
Sub.code	Subject Name	Sub.code	Subject Name	
17MCAI-11	Applied Mathematics (For EEE)	CAI-11/TPE-11	Advanced Mathematics	30% Change in the syllabus
18MCA11	Data Structures using C++	MCA101	Advanced Java Programming	100% syllabus changed from 2018 scheme to 2020 scheme. Data Structures kept in 2 nd semester with 5% change in syllabus in 2020 scheme
18MCA12 18MCA33	Web Technologies Advanced Web Programming	MCA103	Advanced Web Programming	Web Technologies and Advanced Web Programming theory subjects are merged and allotted in I semester in 2020 scheme 60 % syllabus deviation from 2018 scheme to 2020 scheme
18MCA13	Unix Programming	MCA102	Operation System	100% syllabus changed from 2018 scheme to 2020 scheme.
18MCA14	Mathematical Structures and Statistics	MCA104	Mathematical Foundation for Computer Application	100% syllabus changed from 2018 scheme to 2020 scheme.
18MCA15	Computer Organization	MCA105	Software Engineering	100% syllabus changed from 2018 scheme to 2020 scheme.
18MCA21 18MCA41	Java Programming Advanced Java Programming	MCA101	Advanced Java Programming	Java Programming and Advanced Java Programming theory subjects are merged and allotted in I semester in 2020 scheme 60 % syllabus deviation from 2018 scheme to 2020 scheme

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18MCA22	Database Management System	MCA203	Database Management System	5% Syllabus changed from 2018 scheme to 2020 scheme and allotted in I semester in 2020 scheme
18MCA23	Operating System	MCA202	Data Structures	100% syllabus changed from 2018 scheme to 2020 scheme. And operating system kept in 1 st semester with 5% change in syllabus in 2020 scheme
18MCA31	Data warehousing and Data mining	MCA301	Data Analytics	100% syllabus changed from 2018 scheme to 2020 scheme.
18MCA32	Python Programming	MCA302	Computer Networks	100% syllabus changed from 2018 scheme to 2020 scheme.
18MCA33	Advanced Web programming	MCA303	Cloud Computing	100% syllabus changed from 2018 scheme to 2020 scheme. And advanced Web Programming kept in I semester with 20% syllabus changed in 2020 scheme.


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